

P.E.S COLLEGE OF ENGINEERING, MANDYA - 571 401

(An Autonomous Institution Affiliated to VTU,Belgaum)

Provisional Results of Second Semester M.Tech. Semester End Examination for the Academic year 2018-19 (June/July -19)

14-06-2019

ELECTRONICS AND COMMUNICATION ENGINEERING (VLSI Design and Embedded Systems)

SL.No.	USN	NAME	P18MECE21			P18MECE22			P18MECE23			P18MECE241			P18MECE252			P18MECEL26			SGPA	CLASS																								
			C	S	T	CR	GP	GR	C	S	T	CR	GP	GR	C	S	T	CR	GP	GR			C	S	T	CR	GP	GR	C	S	T	CR	GP	GR	C	S	T	CR	GP	GR						
1	4PS18LVS01	ARPITHA M	43	25	68	4.0	7	C	49	36	85	4.0	9	A	42	36	78	4.0	8	B	37	26	63	4.0	7	C	38	33	71	4.0	8	B	46	33	79	2.0	8	B							7.82	FCD
2	4PS18LVS02	KUMAR B R	45	35	80	4.0	9	A	49	30	79	4.0	8	B	46	40	86	4.0	9	A	36	24	60	4.0	7	C	42	36	78	4.0	8	B	47	33	80	2.0	9	A							8.27	FCD
3	4PS18LVS03	MANOJKUMAR	45	29	74	4.0	8	B	49	39	88	4.0	9	A	45	46	91	4.0	10	S	36	31	67	4.0	7	C	45	39	84	4.0	9	A	46	31	77	2.0	8	B							8.55	FCD
4	4PS18LVS04	PAVITHRA B S	37	21	58	4.0	6	D	44	35	79	4.0	8	B	46	46	92	4.0	10	S	41	31	72	4.0	8	B	44	35	79	4.0	8	B	49	40	89	2.0	9	A							8.09	FCD
5	4PS18LVS05	PREEETHI A B	42	22	64	4.0	7	C	41	32	73	4.0	8	B	44	35	79	4.0	8	B	40	20	60	4.0	7	C	40	29	69	4.0	7	C	46	40	86	2.0	9	A							7.55	FC
6	4PS18LVS06	PRIYADARSHINI N J	45	23	68	4.0	7	C	44	40	84	4.0	9	A	45	45	90	4.0	10	S	37	21	58	4.0	6	D	42	25	67	4.0	7	C	48	44	92	2.0	10	S							8.00	FCD
7	4PS18LVS07	RAKSHITHA G V	43	20	63	4.0	7	C	47	36	83	4.0	9	A	44	42	86	4.0	9	A	41	25	66	4.0	7	C	42	36	78	4.0	8	B	45	39	84	2.0	9	A							8.09	FCD
8	4PS18LVS08	RAKSHITHA S	36	20	56	4.0	6	D	41	31	72	4.0	8	B	41	27	68	4.0	7	C	39	22	61	4.0	7	C	37	26	63	4.0	7	C	46	37	83	2.0	9	A							7.18	FC
9	4PS18LVS09	S P VIGNESH	32	21	53	4.0	6	D	44	38	82	4.0	9	A	39	44	83	4.0	9	A	35	26	61	4.0	7	C	37	26	63	4.0	7	C	47	37	84	2.0	9	A							7.73	FC
10	4PS18LVS10	SINDHU J	30	21	51	4.0	6	D	43	40	83	4.0	9	A	46	42	88	4.0	9	A	33	29	62	4.0	7	C	34	26	60	4.0	7	C	49	45	94	2.0	10	S							7.82	FCD
11	4PS18LVS11	SINDHUSHREE J M	43	31	74	4.0	8	B	49	35	84	4.0	9	A	45	36	81	4.0	9	A	43	25	68	4.0	7	C	39	29	68	4.0	7	C	47	44	91	2.0	10	S							8.18	FCD
12	4PS18LVS12	SOWMYASHREE A N	36	32	68	4.0	7	C	40	37	77	4.0	8	B	44	39	83	4.0	9	A	29	27	56	4.0	6	D	37	35	72	4.0	8	B	49	39	88	2.0	9	A							7.73	FC
13	4PS18LVS13	YUVASHREE G S	42	34	76	4.0	8	B	48	43	91	4.0	10	S	45	45	90	4.0	10	S	36	37	73	4.0	8	B	40	34	74	4.0	8	B	49	39	88	2.0	9	A							8.82	FCD

P18MECE21 :- Design of Analog and Mixed Mode VLSI Circuits

P18MECEL26 :- Laboratory - II

P18MECE22 :- Low Power VLSI Design

P18MECE23 :- Real Time Systems

P18MECE241 :- ARM Processor

P18MECE252 :- Design of VLSI System

C - CIE, S - SEE, T - CIE+SEE, CR -Credits, GP - Grade Point, GR - Grade, 111 - Absent

Transitional Grades: I - Course Incomplete

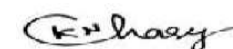
W - Withdrawn from the Course N - Not Eligible w.r.t either CIE / Attendance

X - CIE > 44, Satisfactory Attendance and SEE < 20

FCD - First Class with Distinction, FC - First Class, SC - Second Class, PC - Pass Class, FL - Fail

Percentage Equivalence of Grade Points (10 - Point Scale)

SGPA	Percentage of Marks / Class (SGPA - 0.75) x 10	Level	Out - standing	Excellent	Very Good	Good	Average	Poor	Fail
5.75	50 (Second Class)	Grade	S	A	B	C	D	E	F
6.25	55	Grade Points	10	09	08	07	06	04	00
6.75	60 (First Class)	Raw Score	90 Above	80-89	70-79	60-69	50-59	40-49	Below 40
7.25	65								
7.75	70 (Distinction)								
8.25	75								



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Note : Candidates are informed to bring to the notice of COE for any discrepancy, if any, within three days from the date of announcement of results. Subsequent requests will not be entertained.